

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|--|---|------------------|---------|------------------|
| S1 | 1 | "20040264227" | US-PGPUB; USPAT | OR | OFF | 2006/04/10 10:09 |
| S9 | 364 | (Kojima ADJ Hirotsugu Kitamura ADJ Teruo).in. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:10 |
| S8 | 47 | (Kojima ADJ Hirotsugu Kitamura ADJ Teruo).in. | USPAT | OR | OFF | 2006/04/10 10:11 |
| S13 | 1 | RENESAS adj TECHNOLOGY.as. and (Kojima ADJ Hirotsugu Kitamura ADJ Teruo).in. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2006/04/10 10:13 |
| S14 | 10716 | RENESAS adj TECHNOLOGY.as. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2006/04/10 10:14 |
| S15 | 29 | S14 and frequency with adjust\$4 with oscillator | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2006/04/10 10:17 |
| S18 | 1 | S9 and (frequency with adjust\$4 with oscillator).clm. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:18 |
| S17 | 4 | S14 and (frequency with adjust\$4 with oscillator).clm. | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:18 |
| S21 | 11 | S14 and frequency with adjust\$4 with oscillator and voltage adj control | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:19 |
| S23 | 9 | S14 and frequency with adjust\$4 and voltage adj control adj oscillator | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:20 |
| S22 | 2 | S14 and frequency with adjust\$4 with voltage adj control adj oscillator | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:20 |
| S16 | 14 | S14 and frequency with adjust\$4 near3 oscillator | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2006/04/10 10:21 |

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| S26 | 1 | S14 and frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and JTAG | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:22 |
| S25 | 10 | S14 and frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:22 |
| S27 | 1 | S14 and frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and "TAP" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:24 |
| S29 | 29 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and JTAG adj interface | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:25 |
| S28 | 1 | S14 and frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and JTAG adj interface | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:25 |
| S30 | 25 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and JTAG adj interface and @ad<"20040414" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:26 |
| S31 | 26 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and JTAG adj interface and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:27 |
| S34 | 6 | frequency with adjust\$4 near3 oscillator and phase adj lock\$3 adj loop and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:52 |
| S37 | 1962 | frequency with adjust\$4 near3 oscillator and phase adj lock\$3 adj loop and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:58 |
| S36 | 3699 | frequency with adjust\$4 with oscillator and phase adj lock\$3 adj loop and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 10:58 |
| S38 | 350 | frequency with adjust\$4 near3 oscillator with phase adj lock\$3 adj loop and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 11:02 |
| S39 | 47 | frequency with adjust\$4 near3 oscillator with phase adj lock\$3 adj loop and baseband and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 11:05 |

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| S40 | 15 | frequency with adjust\$4 near3 oscillator with phase adj lock\$3 adj loop and baseband and IEEE and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 11:06 |
| S41 | 59 | frequency with adjust\$4 near3 oscillator with phase adj lock\$3 adj loop and IEEE and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 11:07 |
| S42 | 27 | frequency with adjust\$4 near3 oscillator with phase adj lock\$3 adj loop and integrated adj circuit and IEEE and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 11:09 |
| S35 | 17 | frequency with adjust\$4 with oscillator and phase adj lock\$3 adj loop and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:37 |
| S44 | 39 | frequency with adjust\$4 with oscillator and "ID" and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:40 |
| S43 | 6 | frequency with adjust\$4 with oscillator and phase adj lock\$3 adj loop and "ID" and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:40 |
| S46 | 25 | frequency with adjust\$4 with oscillator and "ID" near (chip IC) and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:41 |
| S45 | 26 | frequency with adjust\$4 with oscillator and "ID" with (chip IC) and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:41 |
| S47 | 25 | frequency with adjust\$4 with oscillator and identif\$5 near (chip IC) and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:43 |
| S50 | 2 | frequency with adjust\$4 with oscillator and "ID" adj register and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:44 |
| S48 | 7977 | frequency with adjust\$4 with oscillator and identif\$5 near part and JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:44 |
| S56 | 4 | identif\$5 near register with JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:54 |

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| S55 | 2 | identif\$5 near3 register near3 JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:54 |
| S57 | 12 | identif\$5 near3 register with JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:59 |
| S53 | 5 | identif\$5 near3 (chip IC) with JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 12:59 |
| S24 | 10 | S14 and frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:45 |
| S58 | 906 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 with recei\$3) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:46 |
| S60 | 108 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 with recei\$3) with "RF" and (integrated adj circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:47 |
| S59 | 178 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 with recei\$3) with "RF" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:47 |
| S62 | 87 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 near3 recei\$3) with "RF" and (integrated adj circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:48 |
| S61 | 240 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 ner3 recei\$3) with "RF" and (integrated adj circuit) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 14:48 |
| S65 | 47 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 near3 recei\$3) with "RF" and (integrated adj circuit) and @ad<"20040415" | USPAT | OR | ON | 2006/04/10 14:53 |
| S68 | 14 | frequency with adjust\$4 near3 oscillator SAME (phase adj locked adj loop PLL) and (transmi\$5 near3 recei\$3) with "RF" and (integrated adj circuit) and @ad<"20040415" | USPAT | OR | ON | 2006/04/10 14:54 |

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| S67 | 7 | frequency with adjust\$4 near3 oscillator WITH (phase adj locked adj loop PLL) and (transmi\$5 near3 recei\$3) with "RF" and (integrated adj circuit) and @ad<"20040415" | USPAT | OR | ON | 2006/04/10 14:54 |
| S63 | 48 | frequency with adjust\$4 near3 oscillator and (phase adj locked adj loop PLL) and (transmi\$5 near3 recei\$3) with "RF" and (integrated adj circuit) | USPAT | OR | ON | 2006/04/10 14:56 |
| S69 | 30 | identif\$5 near3 JTAG and @ad<"20040415" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/04/10 18:20 |